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** CONTINUING DATA *****

This application is a CON of 09/856,758 09/15/2001 PAT 6,731,509 *A-C*
which is a 371 of PCT/JP99/06595 11/26/1999

** FOREIGN APPLICATIONS *****

JAPAN 10-339265 11/30/1998 *A-C*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after Allowance Verified and Acknowledged <i>Henry G. Brown</i> <i>A-C</i> Examiner's Signature Initials				

ADDRESS

24956

TITLE

Method of mounting electronic circuit chip

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